



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of: **WATANABE, Kenichi**

Serial No.: 10/050,171

Group Art Unit: 2815

Examiner: **WARREN, Matthew E.**

Filed: **January 18, 2002**

P.T.O. Confirmation No.: 7623

FOR: **SEMICONDUCTOR DEVICE CAPABLE OF SUPPRESSING CURRENT  
CONCENTRATION IN PAD AND ITS MANUFACTURE METHOD**

**RESPONSE TO THE RESTRICTION REQUIREMENT**  
**DATED JULY 15, 2002**

Commissioner for Patents  
Washington, D.C. 20231

Date: August 12, 2002

Sir:

This paper is submitted in response to the Official Action dated **July 15, 2002**.

In the Action, restriction is required between Group (I), Claims 7-13, drawn to a semiconductor device; and Group (II), Claims 1-6, drawn to a method of making a semiconductor.

Applicant hereby elects the subject matter of Group (I), Claims 7-13 for prosecution in this application. This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

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In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



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WGK/nrp

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